

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	243	(shut\$\$down near4 grace\$\$ful)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/22 17:52
S3	29	(shut\$\$down near4 grace\$\$ful) and (host near4 processor) and circuit	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/22 17:53
S2	35	(shut\$\$down near4 grace\$\$ful) and (host near4 processor)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/22 17:53
S4	10	(shut\$\$down same grace\$\$ful).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/22 17:56
S5	17	(shut\$\$down same grace\$\$ful).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/23 16:06
S7	3	(shut\$\$down near10 grace\$\$ful) and (hot\$\$swap)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/23 16:09
S6	17	(shut\$\$down same grace\$\$ful).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/23 16:09
S9	54	(shut\$\$down near10 grace\$\$ful) and (embed\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/23 16:11
S8	14	(shut\$\$down near10 grace\$\$ful) and (hot near5 swap\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/23 16:11
S10	3	(shut\$\$down near10 grace\$\$ful) and (embed\$4) and (processor near4 card)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/23 16:13
S11	7	(shut\$\$down near10 grace\$\$ful) and (processor near4 card)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/23 16:14

## EAST Search History

S12	11	(shut\$\$down near10 grace\$\$ful near10 circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/23 16:17
S13	37	(shut\$\$down near10 grace\$\$ful) and (host near4 processor)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/23 16:22
S14	29	(shut\$\$down near10 grace\$\$ful) and swap\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/23 16:25
S16	37	(shut\$\$down near10 grace\$\$ful) and (host near4 processor)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 11:32
S15	7	(shut\$\$down near10 grace\$\$ful) and (hot near4 plug\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 11:32
S17	10	(shut\$\$down) and (host near4 processor) and (monitor\$3 near4 circuit) and grace\$\$ful	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 11:33
S19	16	(shut\$\$down) and (host near4 processor) and grace\$\$ful and (hot near4 swap\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 11:35
S20	0	(shut\$\$down) and (host near4 processor) and grace\$\$ful and (hot near4 plug\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 11:36
S18	93	(shut\$\$down) and (host near4 processor) and grace\$\$ful	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 11:36
S21	12	(shut\$\$down) and (host near4 processor) and grace\$\$ful and (power near4 state)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 11:39
S23	327	((shut\$\$down or (shut near4 down)) same grace\$\$ful)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 11:41
S22	12	((shut\$\$down or (shut near4 down)) same host same processor same grace\$\$ful)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 11:41

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S24	53	((shut\$\$down or (shut near4 down)) same grace\$\$ful) and (host near4 processor)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 11:46
S25	10	((shut\$\$down or (shut near4 down)) same grace\$\$ful).ti.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 11:47
S26	18	((shut\$\$down or (shut near4 down)) same grace\$\$ful).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 11:49
S28	327	((shut\$\$down or (shut near4 down)) same grace\$\$ful)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 11:50
S27	23	((shut\$\$down or (shut near4 down)) same grace\$\$ful).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 11:50
S29	75	((shut\$\$down or (shut near4 down)) same grace\$\$ful) and embed\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 11:57
S31	8	"5781770"	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 14:49
S30	57	((shut\$\$down or (shut near4 down)) same grace\$\$ful) and (power near4 manag\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 14:49
S32	3	("5329579"   "5594893"   "5596711").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/01/24 15:06
S33	0	(self near4 (shut\$\$down or (shut\$4 near4 down)) near4 grace\$\$ful)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 15:07
S34	1	(self same (shut\$\$down or (shut\$4 near4 down)) same grace\$\$ful)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 15:16
S36	8	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful) same card	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 15:17

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S38	19	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful) same circuit	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 15:34
S37	45	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful) same processor	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 15:34
S35	276	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 15:37
S39	54	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful) and "709"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 15:40
S40	0	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful) and "395"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 15:41
S42	7	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful) and "307"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 15:42
S41	25	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful) and "370"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 15:42
S44	600	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 16:10
S43	34	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful) and "713"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 16:10
S45	17	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful\$2 near5 processor)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 16:11
S47	15	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful\$2) and (hot near4 plug\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 16:13
S46	39	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful\$2) and (hot near4 swap\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 16:13

## EAST Search History

S48	24	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful\$2 near5 circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 16:14
S50	92	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful\$2) and (host near4 processor)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 16:16
S49	25	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful\$2).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 16:16
S51	6	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful\$2) and (host near4 processor near4 card)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/24 16:20
S52	18	((shut\$\$down or (shut\$4 near4 down)) near5 grace\$\$ful\$2) and (host near4 processor) and (power near4 manag\$6)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/25 14:50
S53	1	("7099934").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/01/25 15:45
S54	1	("20020069371").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/01/25 15:50
S56	1	"6598175"	US-PGPUB; USPAT; USOCR	OR	ON	2007/01/25 15:54
S55	1	("6230181").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/01/25 15:54
S57	4	("5594893"   "5596711"   "5781770"   "6304981").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/01/25 16:01
S58	1	("5923099").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/01/25 16:23
S60	1	("6810363").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/01/25 16:55
S59	1	("20020016818").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/01/25 16:55
L2	5	((shut\$\$down or (shut near4 down)) near5 grace\$\$ful) and (host near4 card)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/05/12 13:36

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L1	7	((shut\$\$down or (shut near4 down)) near5 grace\$\$ful) and (processor near4 card)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/05/12 13:36
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## » Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

- ☐ 1. Do we need anything more than single bit error correction (ECC)?  
Spica, M.; Mak, T.M.;  
Memory Technology, Design and Testing, 2004. Records of the 2004 International Work  
9-10 Aug. 2004 Page(s):111 - 116  
Digital Object Identifier 10.1109/MTDT.2004.1327993  
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IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

- ☐ 1. Voltage interface issues for PCMCIA sockets  
LaPorte, D.;  
[WESCON/95. Conference record. 'Microelectronics Communications Technology Produ Quality Products Mobile and Portable Power Emerging Technologies'](#)  
7-9 Nov. 1995 Page(s):153  
Digital Object Identifier 10.1109/WESCON.1995.485269  
[AbstractPlus](#) | Full Text: [PDF](#)(496 KB) IEEE CNF  
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- ☐ 2. Unique control schemes extend battery life in portable equipment  
Vargha, D.;  
[WESCON/93. Conference Record.](#)  
28-30 Sept. 1993 Page(s):396 - 406  
Digital Object Identifier 10.1109/WESCON.1993.488468  
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**4 Selected writings on computing: a personal perspective**


Edsger W. Dijkstra

January 1982 Book

**Publisher:** Springer-Verlag New York, Inc.

Full text available: pdf(60.98 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Since the summer of 1973, when I became a Burroughs Research Fellow, my life has been very different from what it had been before. The daily routine changed: instead of going to the University each day, where I used to spend most of my time in the company of others, I now went there only one day a week and was most of the time that is, when not travelling!-- alone in my study. In my solitude, mail and the written word in general became more and more important. The circumstance that my employe ...

## 5 RAID: high-performance, reliable secondary storage



Peter M. Chen, Edward K. Lee, Garth A. Gibson, Randy H. Katz, David A. Patterson  
June 1994 **ACM Computing Surveys (CSUR)**, Volume 26 Issue 2

**Publisher:** ACM Press

Full text available: pdf(3.60 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Disk arrays were proposed in the 1980s as a way to use parallelism between multiple disks to improve aggregate I/O performance. Today they appear in the product lines of most major computer manufacturers. This article gives a comprehensive overview of disk arrays and provides a framework in which to organize current and future work. First, the article introduces disk technology and reviews the driving forces that have popularized disk arrays: performance and reliability. It discusses the tw ...

**Keywords:** RAID, disk array, parallel I/O, redundancy, storage, striping

## 6 Classics in software engineering



January 1979 Divisible Book

**Publisher:** Yourdon Press

Full text available: pdf(22.45 MB)

Additional Information: [full citation](#), [cited by](#), [index terms](#)

## 7 Special issue on knowledge representation



Ronald J. Brachman, Brian C. Smith  
February 1980 **ACM SIGART Bulletin**, Issue 70

**Publisher:** ACM Press

Full text available: pdf(13.13 MB)

Additional Information: [full citation](#), [abstract](#), [citations](#)

In the fall of 1978 we decided to produce a special issue of the SIGART Newsletter devoted to a survey of current knowledge representation research. We felt that there were two useful functions such an issue could serve. First, we hoped to elicit a clear picture of how people working in this subdiscipline understand knowledge representation research, to illuminate the issues on which current research is focused, and to catalogue what approaches and techniques are currently being developed. Secon ...

## 8 Yellow Dog Linux on the iMac



Stew Benedict

August 2000 **Linux Journal**

**Publisher:** Specialized Systems Consultants, Inc.

Full text available: html(22.13 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

A guide to installing and running YDL on a power PC.

## 9 SIGART special issue on machine learning



April 1981 **ACM SIGART Bulletin**, Issue 76

**Publisher:** ACM Press

Full text available: pdf(3.33 MB)

Additional Information: [full citation](#), [abstract](#)

Current research on Machine Learning encompasses a diverse set of approaches, and of opinions regarding where the important issues lie. The significant increase of interest and research activity in Machine Learning over the past few years has led us to organize this special issue of SIGART, whose purpose is to provide a snapshot of current research in this field. This issue contains a set of summaries of ongoing research, solicited from the

community at large, and received from thirty-five resea ...

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 4 [Curriculum 68: Recommendations for academic programs in computer science: a report of the ACM curriculum committee on computer science](#)


William F. Atchison, Samuel D. Conte, John W. Hamblen, Thomas E. Hull, Thomas A. Keenan, William B. Kehl, Edward J. McCluskey, Silvio O. Navarro, Werner C. Rheinboldt, Earl J. Schweppe, William Viavant, David M. Young

March 1968 **Communications of the ACM**, Volume 11 Issue 3

Publisher: ACM Press

Full text available: [pdf\(6.63 MB\)](#)Additional Information: [full citation](#), [references](#), [citations](#)
**Keywords:** computer science academic programs, computer science bibliographies, computer science courses, computer science curriculum, computer science education,

computer science graduate programs, computer science undergraduate programs

5 Selected writings on computing: a personal perspective

Edsger W. Dijkstra

January 1982 Book

**Publisher:** Springer-Verlag New York, Inc.

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6 Classics in software engineering

January 1979 Divisible Book

**Publisher:** Yourdon Press

Full text available:  pdf(22.45 MB)

Additional Information: [full citation](#), [cited by](#), [index terms](#)

7 The information furnace: consolidated home control

Diomidis D. Spinellis

May 2003 **Personal and Ubiquitous Computing**, Volume 7 Issue 1

**Publisher:** Springer-Verlag

Full text available:  pdf(488.36 KB)

Additional Information: [full citation](#), [abstract](#), [citings](#), [index terms](#)

The Information Furnace is a basement-installed PC-type device that integrates existing consumer home-control, infotainment, security and communication technologies to transparently provide accessible and value-added services. A modern home contains a large number of sophisticated devices and technologies. Access to these devices is currently provided through a wide variety of disparate interfaces. As a result, end users face a bewildering array of confusing user-interfaces, access modes a ...

**Keywords:** Automation, Consumer electronics, Home-control, Multi-modal interfaces

8 Hiperlan/2 public access interworking with 3G cellular systems

Stephen McCann, Helena Flygare

January 2004 **Wireless Networks**, Volume 10 Issue 1

**Publisher:** Kluwer Academic Publishers

Full text available:  pdf(196.64 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper discusses some of the recent work within the Broadband Radio Access Network (BRAN) project of ETSI (European Telecommunications Standards Institute), regarding the issue of establishing an interworking solution between Hiperlan/2 and 3G cellular systems and introduces the concept of Hiperlan/2 Public Access. So far the BRAN project has identified several fundamentally different types of possible solutions whose differences lie within the level of integration. The most essential requir ...

**Keywords:** 3G, BRAN, ETSI, Hiperlan/2, WIG, interworking, public access

### 9 Yellow Dog Linux on the iMac

Stew Benedict

August 2000 **Linux Journal**

**Publisher:** Specialized Systems Consultants, Inc.

Full text available:  [html\(22.13 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

A guide to installing and running YDL on a power PC.


### 10 Distributed operating systems



Andrew S. Tanenbaum, Robbert Van Renesse

December 1985 **ACM Computing Surveys (CSUR)**, Volume 17 Issue 4

**Publisher:** ACM Press

Full text available:  [pdf\(5.49 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Distributed operating systems have many aspects in common with centralized ones, but they also differ in certain ways. This paper is intended as an introduction to distributed operating systems, and especially to current university research about them. After a discussion of what constitutes a distributed operating system and how it is distinguished from a computer network, various key design issues are discussed. Then several examples of current research projects are examined in some detail ...

### 11 Fine-Grained Dynamic Voltage and Frequency Scaling for Precise Energy and Performance Trade-Off Based on the Ratio of Off-Chip Access to On-Chip Computation Times

Kihwan Choi, Ramakrishna Soma, Massoud Pedram

February 2004 **Proceedings of the conference on Design, automation and test in Europe - Volume 1 DATE '04**

**Publisher:** IEEE Computer Society

Full text available:  [pdf\(757.37 KB\)](#) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

This paper presents an intra-process dynamic voltage and frequency scaling (DVFS) technique targeted toward non real-time applications running on an embedded system platform. The key idea is to make use of runtime information about the external memory access statistics in order to perform CPU voltage and frequency scaling with the goal of minimizing the energy consumption while translucently controlling the performance penalty. The proposed DVFS technique relies on dynamically-constructed regres ...

### 12 Concepts of building a design automation system



D. Lyons, M. McLuhan

January 1966 **Proceedings of the SHARE design automation project DAC '66**

**Publisher:** ACM Press

Full text available:  [pdf\(1.34 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

"....automation creates roles for people, which is to say depth of involvement in their work and human association that our preceding mechanical technology had destroyed." There are many terms used for the computer application to the aid of design and production, none of which truly state the case. We are concerned here with a system that will implement the ideas behind these terms. The tools used for this implementation are the computers and a large amount of software ...

### 13 SIGART special issue on machine learning



April 1981 **ACM SIGART Bulletin**, Issue 76

**Publisher:** ACM Press

Full text available:  [pdf\(3.33 MB\)](#) Additional Information: [full citation](#), [abstract](#)

Current research on Machine Learning encompasses a diverse set of approaches, and of opinions regarding where the important issues lie. The significant increase of interest and research activity in Machine Learning over the past few years has led us to organize this special issue of SIGART, whose purpose is to provide a snapshot of current research in this field. This issue contains a set of summaries of ongoing research, solicited from the community at large, and received from thirty-five resea ...

14 RAID: high-performance, reliable secondary storage



Peter M. Chen, Edward K. Lee, Garth A. Gibson, Randy H. Katz, David A. Patterson  
June 1994 **ACM Computing Surveys (CSUR)**, Volume 26 Issue 2

**Publisher:** ACM Press

Full text available: [pdf\(3.60 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Disk arrays were proposed in the 1980s as a way to use parallelism between multiple disks to improve aggregate I/O performance. Today they appear in the product lines of most major computer manufacturers. This article gives a comprehensive overview of disk arrays and provides a framework in which to organize current and future work. First, the article introduces disk technology and reviews the driving forces that have popularized disk arrays: performance and reliability. It discusses the tw ...

**Keywords:** RAID, disk array, parallel I/O, redundancy, storage, striping

15 The Tyndall range control system: bringing network computing to C2 systems



Dan DeJohn  
November 1994 **Proceedings of the conference on TRI-Ada '94 TRI-Ada '94**

**Publisher:** ACM Press

Full text available: [pdf\(1.24 MB\)](#) Additional Information: [full citation](#), [index terms](#)

16 Special issue on knowledge representation



Ronald J. Brachman, Brian C. Smith  
February 1980 **ACM SIGART Bulletin**, Issue 70

**Publisher:** ACM Press

Full text available: [pdf\(13.13 MB\)](#) Additional Information: [full citation](#), [abstract](#), [citations](#)

In the fall of 1978 we decided to produce a special issue of the SIGART Newsletter devoted to a survey of current knowledge representation research. We felt that there were two useful functions such an issue could serve. First, we hoped to elicit a clear picture of how people working in this subdiscipline understand knowledge representation research, to illuminate the issues on which current research is focused, and to catalogue what approaches and techniques are currently being developed. Secon ...

17 Preprocessors in a data communication computer environment



David L. Mills  
October 1969 **Proceedings of the first ACM symposium on Problems in the optimization of data communications systems**

**Publisher:** ACM Press

Full text available: [pdf\(1.26 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Realizing the need for a highly adaptable transmission control unit to interface varied terminal equipment to the Michigan Timesharing System (MTS), the University of Michigan initiated in 1965 the development of a special control unit to be used in conjunction with the System/360 Model 67. Called the Data Concentrator The design approach taken in the Data Concentrator has been to nucleate about a small general-



purpose computer a number of special-purpose interfaces to the variou ...

### 18 Risks to the public: Risks to the public



Peter G. Neumann

September 2006 **ACM SIGSOFT Software Engineering Notes**, Volume 31 Issue 5

**Publisher:** ACM Press

Full text available: pdf(281.86 KB) Additional Information: [full citation](#), [abstract](#), [index terms](#)

Edited by Peter G. Neumann (Risks Forum Moderator and Chairman of the ACM Committee on Computers and Public Policy), plus personal contributions by others, as indicated. Opinions expressed are individual rather than organizational, and all of the usual disclaimers apply. We address problems relating to software, hardware, people, and other circumstances relating to computer systems. To economize on space, we include pointers to items in the online Risks Forum: (R i j) denotes RISKS vol i number ...

### 19 A survey of current microprogrammed computers



C. V. Ramamoorthy

June 1972 **Proceedings of the 9th workshop on Design automation DAC '72**

**Publisher:** ACM Press

Full text available: pdf(201.27 KB) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

This paper summarizes the current status of microprogrammed computers. We shall attempt only a deliberately simplified overview of the subject.

### 20 The Vidboard: a video capture and processing peripheral for a distributed multimedia system



Joel F. Adam, David L. Tennenhouse

September 1993 **Proceedings of the first ACM international conference on Multimedia MULTIMEDIA '93**

**Publisher:** ACM Press

Full text available: pdf(442.09 KB) ps(911.68 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

**Keywords:** ATM, digital video, distributed systems, framegrabber, gigabit networks, video capture

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### **System and method for graceful shutdown of host processor cards in ...**

The **host processor card** of claim 1, wherein the **graceful shutdown circuit** further comprises a **monitor circuit** coupled to the **power control line** and coupled ...

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### **Management shutdown and reset of embedded systems - Patent 6230181**

The management reset is received and the **operating system** is shutdown in response. ...

The gateway **card** 50 includes a debounce **circuit** 36 and a shutdown and ...

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**Graceful shutdown** support – Certain service processors support a remote power control command that actually sends a signal to the server **OS** to shut down ...

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tors with access to servers and **control** of the server hardware and **OS** from ... However, for top-of-the-line xSeries servers, the service **processor** of choice ...

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**Line cards** used with the SSO feature periodically generate **status** events that ... ROM **monitor** and thereby taking the **system** out of the remote user **control**. ...

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to **boot** automatically after a reset, you must abort the **boot** with the ALOM CMT. break command or perform a **graceful shutdown** of the Solaris **operating system** ...

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**status**, front panel LED **status**, **system control** keyswitch position, and so on. The ... automatically **boot** the **operating system** after each reset. ...

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synthesis that allows **graceful shutdown** to prevent **system** damage. ... NiCd/NiMH batteries under **control** from a **host processor**. An. internal low R ...

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components via an inter-integrated **circuit** (I. 2. C) bus to continually **monitor** the **status** of **system** fans, temperature, and power supplies; it signals the ...

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Once a **processor** is deallocated, the HP-UX **operating system** ... If the peripheral **status monitor** (PSM) has been properly configured, events are also ...

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execute different **operating systems**—such as HP-UX, Windows NT ... Monarch

**processor** refers to one **processor** that is selected during **system boot** and given ...

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When the **operating system** crashes, the **system** reverts to OpenBoot firmware. **control**.

■. During the **boot** process, when there is a serious hardware problem ...

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firmware **control**. If the **operating system** is already halted, ... device and tries the next-in-  
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